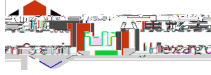




The diagram illustrates the internal architecture of the Cortex-M0+ core. It is divided into several main sections:

- Core Components (Top):** A large light blue rectangle represents the core's internal logic. To its right, an orange rectangle contains four smaller light blue boxes labeled "DSP", "ECLIC", "16KB ICache", and "16KB DCache".
- System Components (Bottom):** Below the core components, there are three light blue rectangles of varying sizes, representing system components like memory or peripheral controllers. To the right of these, a light green rectangle contains a small light blue horizontal bar, likely representing a bus or interface component.



2



- ☐ RSIC-V 32 CPU
- ☐ FPU DSP 300Mhz
- ☐ 1MB System RAM
- ☐ 8KB Boot ROM
- ☐ 8 DMA



- ☐ DP/eDP RX
- 5.6G*4lane